10580827 - GAU: 2822 Receipt date: 05/26/2006

10/580827 IAP9 Rec'd PCT/PTO 2 6 MAY 2006

Docket No.: R2184.0501/P501

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Masanori Dainin

Application No.: Not Yet Assigned

Confirmation No.: N/A

Filed: Concurrently Herewith

Art Unit: N/A

SEMICONDUCTOR DEVICE AND FABRICATION PROCESS THEREOF Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A summary/abstract translation of the non-English language references is enclosed.

Receipt date: 05/26/2006 10580827 - GAU: 2822

Application No.: Not Yet Assigned Docket No.: R2184.0501/P501

Applicant has not submitted copies of each cited U.S. patent and U.S. patent application as required by 37 CFR 1.98(a)(2)(i), amended October 2004, as the U.S. Patent and Trademark Office has waived this requirement for all U.S. patent applications. Applicant submits herewith copies of foreign and non-patents in accordance with 37 CFR 1.98(a)(2).

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper

Receipt date: 05/26/2006 105/26/2006 105/26/2006

IAP9 Rec'd PCT/PTO 26 MAY 2007

Application No.: Not Yet Assigned

Docket No.: R2184.0501/P501

hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. R2184.0501/P501. A duplicate copy of this paper is enclosed.

Dated: May 26, 2006

Respectfully submitted,

Mark J. Thronson

Registration No.: 33,082

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP 2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicant

Receipt date: 05/26/2006



PTO/SB/08a/b (07-05)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	Not Yet Assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			SCLOSURE	Filing Date	Concurrently Herewith
			APPLICANT	First Named Inventor	Masanori Dainin
	(Use as many sheets as necessary)			Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	R2184.0501/P501

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

		FOREI	3N PATENT I	DOCUMENTS		
Examiner	Cite	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
Initials*	No.1	Country Code ³ -Number ⁴ -Kind Code ⁵ (# known)				7°
	BA	EP-1310998-A2	05-14-2003			
	BB	JP-9-17860-A	01-17-1997			
	BC	JP-2000-340562	12-08-2000			
	BD	JP-2001-284464-A	10-12-2001			
	BE	JP-6-112410-A	04-22-1994			
	BF	JP-3195828-B2	06-01-2001			
	BG	JP-2003-152100-A	05-23-2003			
	BH	JP-2004-146548-A	05-20-2004			

EXAMINER: Initial if reference considered, whether or not clation is in conformance with MPEP 600. Draw the through clatified in the consolected, include copy of this form with next communication to agreement. Applicant's originate in separation produced in contrast in Section Codes of Code

	NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	r Cite No.1 Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		LIFSHITZ, Nadia, "Dependence of the Work-Function Difference Between the Polycilicon Gate and Silicon Substrate on the Doping Level in Polysilicon", IEEE Transactions on Electron Devices, vol. ed. 32, No. 3, March 1985 , pp 617-621				

*EXAMINER. Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

'Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH, /MT/

Examiner	/Michael Trinh/	Date 09/29/2008
Signature	AMONGO THINK	Considered